

Flexible IGZO thin-film transistors with liquid EGaIn gate contacts

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I. SUMMARY AND MOTIVATION

Flexible thin-film electronics leverage technological innovations in fields such as sensors, wearable computing and healthcare. As these systems are required to conform to non-planar surfaces, novel approaches are developed to provide stable performance under mechanical stress, and prevent crack formation. Liquid eutectic-GaIn promises the realisation of self-healing, reconfigurable and bendable circuits. Here, a liquid EGaIn-gate thin-film transistor is fabricated and characterised. The device yielded a carrier mobility of $7.9 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$ that increased by $0.36 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$ when bent to a 4 mm radius. These results promote the integration of highly deformable liquid materials into thin-film devices.

II. INTRODUCTION

The conformability of flexible electronic systems enables the introduction of imperceptible and ubiquitous devices into a growing number of use cases [1]. In this regard, amorphous Indium-Gallium-Zinc-Oxide (IGZO) thin-film transistors (TFT) have been used to realise flexible electronics for deformable sensor systems used in wearable computing or biomedical applications [2, 3]. IGZO TFTs can be fabricated on various substrates and offer excellent electrical performance with a carrier mobility in the range of $\approx 15 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$ [4]. Nevertheless, the limited ductility of polycrystalline metal thin films restricts the performance of TFTs when bent, as strain leads to the formation of cracks. This is in particular relevant for the thinner areas of these devices such as the gate contacts [5, 6]. In this context, the self-healing and elastic properties of liquid eutectic GaIn (EGaIn) enable the implementation of this metal alloy as highly conformable electric interconnects [7]. This already led to TFT arrays supporting tensile strains up to 40% [8]. At the same time the use of this material as an integral part of active devices is a new field of research. Other liquid-phase materials such as ionic liquids have only been applied as high capacitance gate dielectrics, or to dynamically dope the TFT semiconductor [9, 10]. Herein, we propose a novel approach, where liquid EGaIn is used to modulate the semiconductor channel of a flexible IGZO TFT. This approach constitutes an advancement in the realisation of semi-liquid structures for self-healing electronic circuits.

III. DEVICE STRUCTURE AND FABRICATION

Fig. 1a shows the structure of the transistor fabricated on a free-standing 50 μm thick polyimide foil covered with 50 nm

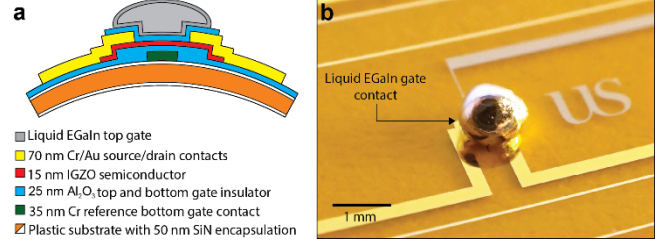


Fig. 1. Flexible IGZO thin film transistor with liquid EGaIn gate contact. a) Schematic cross-section. b) Device micrograph.

SiN. All layers were structured using standard UV lithography. The semiconductor is a 15 nm thick IGZO layer RF-sputtered at room temperature. It is contacted using e-beam evaporated Ti/Au (10 nm/60 nm) source drain (S/D) contacts. The TFT width/length ratio is $560 \mu\text{m}/60 \mu\text{m}$. The channel is insulated using 25 nm thick Al_2O_3 deposited by atomic layer deposition at 150°C . More details on the fabrication can be found in [11]. The channel conductance is modulated by a liquid EGaIn droplet acting as top gate. This droplet was placed on the channel using a micro manipulator (Fig. 1b). The 1 nm thick natural gallium oxide layer covering this droplet has no significant influence on the capacitive coupling between the liquid gate and the semiconductor channel [12]. Additionally, an independent 35 nm Cr bottom gate was used as a reference. However, this gate has a width of only $40 \mu\text{m}$, which results in a total negative S/D overlap of $20 \mu\text{m}$. This negative overlap ensures that the reference and EGaIn gate will act independently on the semiconductor channel, and minimises parasitic coupling between the gates.

IV. RESULTS AND DISCUSSION

The electrical characterisation of the device was carried out using a Keysight B1500A parameter analyser. All measurements were conducted in the dark at room temperature. Performance parameters were extracted from the saturation regime using the Shichman-Hodges model [13].

A. Transistor performance

Fig. 2a illustrates the capacitive coupling between the liquid EGaIn gate, as well as the reference gate with the semiconductor channel. The measured transfer and output characteristics for both configurations are presented in Figs. 2b to 2e. From the reference TFT transfer curve a threshold voltage (V_{th}) of -0.53 V , an effective field-effect mobility (μ_{FE}) of $0.12 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$, a subthreshold swing (SS) of

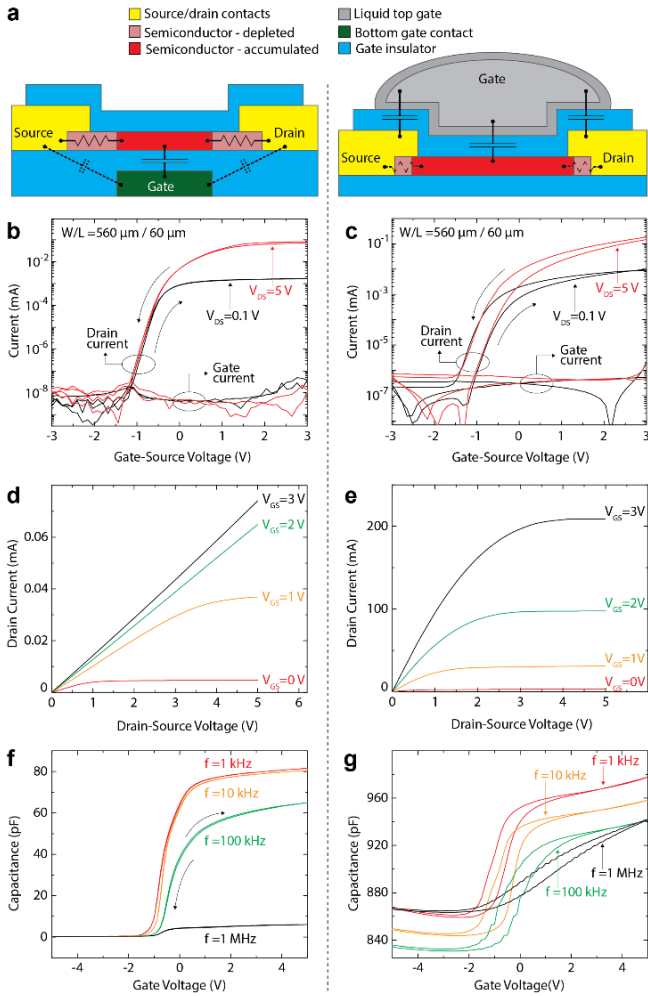


Fig. 2. DC device performance of the reference gate operation mode (left column), and the liquid EGaIn mode (right column). a) Visualisation of the coupling properties between the channel and the contacts. b-g) Transfer, output and CV (with the S/D contacts shorted, and measured for different excitation frequencies) characteristics.

240 mV/dec., a I_{ON}/I_{OFF} current ratio $>10^7$, and a specific transconductance g_m/W ($V_{GS} = 3$ V and $V_{DS} = 5$ V) of $6.8 \text{ mS}\mu\text{m}^{-1}$ were extracted. The gate leakage current is <10 pA and no hysteresis is observed. As illustrated in Fig. 2a (left), the low effective mobility is the result of the negative gate-to-S/D overlaps causing no overlap capacitance (Fig. 2f) and a high contact resistance of $\approx 12 \text{ k}\Omega$ [14]. This is also visible in the corresponding output characteristic, which shows a maximum specific drain conductance g_d/W of $27.5 \text{ mS}\mu\text{m}^{-1}$. These values result in an intrinsic transistor gain of 0.25.

In comparison, the EGaIn-gate TFT presents a V_{th} of -0.83 V, a μ_{FE} of $7.9 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$, a I_{ON}/I_{OFF} ratio $>10^5$, a specific transconductance ($V_{GS} = 3$ V and $V_{DS} = 5$ V) of $187.5 \text{ mS}\mu\text{m}^{-1}$, and a specific output conductance of only $66 \mu\text{S}\mu\text{m}^{-1}$, which results in an intrinsic gain of 2840. The gate leakage current of this device is also <10 pA, which shows that the liquid EGaIn does not deteriorate the Al_2O_3 gate insulator. The observed performance improvement for the EGaIn-gate operation mode reflects the improved capacitive coupling compared to the reference gate (Fig. 2a right). As a result, the larger overlap achieved with the droplet enables the complete

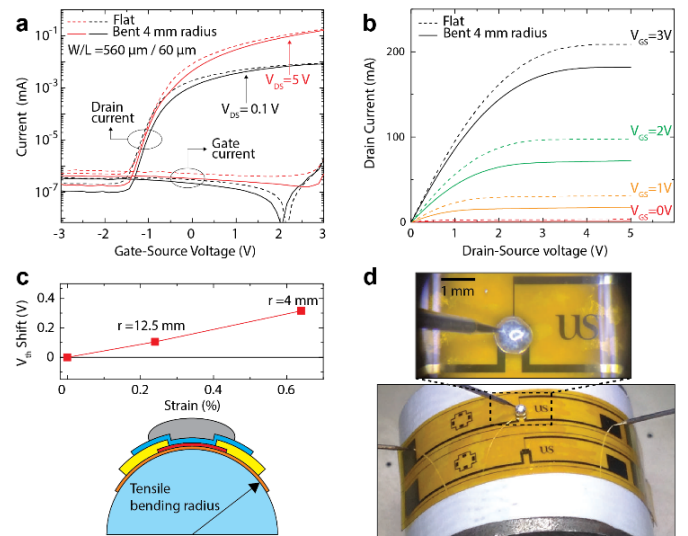


Fig. 3. a) Transfer and b) output characteristics of the flat and bent EGaIn gate TFT. c) Evolution of the threshold voltage. d) Image of the bent device.

modulation of the semiconductor channel leading to a contact resistance of $\approx 1 \text{ k}\Omega$. At the same time, the liquid gate configuration resulted in an increased SS of 469 mV/dec. and a maximum counter-clockwise hysteresis of 0.36 V. As this is not observed for the reference gate, charge trapping at the semiconductor / gate-dielectric interface as an origin for this phenomenon is excluded and indicates that it is related to charge effects at either the gallium-oxide / Al_2O_3 or the EGaIn / gallium-oxide interface [12]. Finally, the capacitance-voltage measurements (Figs. 2f and 2g) show that the channel capacitance ($f = 1$ kHz) increases from 81 pF to 109 pF if the reference gate and EGaIn gate performances are compared. These results are in line with the geometry of the corresponding gates. Furthermore, the size of the EGaIn droplet causes an overlap capacitance of 850 pF.

B. Bending

To study the behaviour of the liquid EGaIn-gate TFT under mechanical strain, the sample was bent using rods with radii of 12.5 mm and 4 mm. Figs. 3a and 3b show the transfer and output curves of the flat and bent (4 mm) liquid gate IGZO TFT. This corresponds to a tensile mechanical strain up to 0.64% [15]. The resulting evolution of the threshold voltage is shown in Fig. 3c. Tensile bending was applied parallel to the channel as illustrated in Figs. 3c and 3d. When bent to a 4 mm radius, V_{th} and μ_{FE} increased by 0.32 V and $0.36 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$, which results in a 10.4% decrease of I_{DS} (Fig. 3b). Comparable strain-induced shifts have previously been observed in conventional IGZO TFTs [16], in particular the μ_{FE} increase is attributed to the reduction of the effective mass under tensile strain, typical for semiconductors [17]. These measurements confirm that the TFT remains functional under strain.

In summary, a flexible amorphous oxide TFT with a liquid EGaIn gate demonstrating a mobility of $7.9 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$, and bendability down to a radius of 4 mm is presented. To the best of our knowledge, this is the first partially liquid IGZO TFT. This shows the feasibility of future semi-liquid TFTs integrated in self-healing, elastic and reconfigurable electronics.

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